AC6966A Datasheet

Zhuhai Jieli Technology Co.,LTD

Version：V1.0

Date：2020.03.14

AC6966A Features

CPU

- 32-bit DSP supports hardware Float Point Unit (FPU)
- Up to 160MHz programmable processor
- 64Vectored interrupts
- 4 Levels interrupt priority

DSP Audio Processing

- SBC, AAC Audio decodes supported for BT audio
- mSBC voice codecs supported for BT phone
- Supports MP2, MP3, WMA, APE, FLAC, AAC, MP4, M4A, WAV, AIF, AIFC audio decoding
- Packet Loss Concealment (PLC) for voice processing
- Acoustic echo cancellation/suppression (AEC,AES)
- Single/Dual MIC Environmental Noise Cancellation (ENC)
- Multi-band DRC limiter
- 10-band EQ configuration for voice Effects

Audio Codec

- Two channels 16-bit DAC, SNR >= 95dB
- One channels 16-bit ADC , SNR >= 90dB
- Sampling rates of 8KHz/11.025KHz/16KHz/22.05KHz/24KHz/ 32KHz/44.1KHz/48KHz are supported
- One analog MIC amplifier, build-in MIC bias generator
- Supports two PDM digital MIC inputs
- Two channels Mono analog MUX
- Supports cap-less, single-ended, and differential mode at the DAC path
- Supports 16ohm and 32ohm Speaker loading

Bluetooth

- Compliant with Bluetooth V5.1+BR+EDR+BLE specification
- Meet class1 class2 and class3 transmitting power requirement
- Support GFSK and π/4 DQPSK all paket types
- Provides +6dbm transmitting power
- receiver with -90dBm sensitivity
- Fast AGC for enhanced dynamic range
- Supports a2dp\avctp\avdtp\avrcp\hfp\spp\smp\att\gap\gatt\rfcomm\sdp\l2cap profile

Peripherals

- One full speed USB 2.0 OTG controller
- Six multi-function 32-bit timers, support capture and PWM mode
- Three full-duplex basic UART, UART0 and UART1 supports DMA mode
- Three SPI interface supports host and device mode
- One hardware IIC interface supports host and device mode
- 10-bit ADC for analog sampling
- External wake up/interrupt on all GPIOs

PMU

- Low voltage LDO for internal digital and analog circuit supply
- 3uA current consumption in the soft-off mode
- Built-in LDO for the core, I/O, Bluetooth and flash
- VBAT is 2.2V to 5.5V
- VDDIO is 2.2V to 3.6V

Packages

- QFN32(4mm*4mm)

Temperature

- Operating temperature: -20℃ to +70℃
- Storage temperature: -65℃ to +150℃

Applications

- Bluetooth TWS Earphone

1、Pin Definition

1.1 Pin Assignment

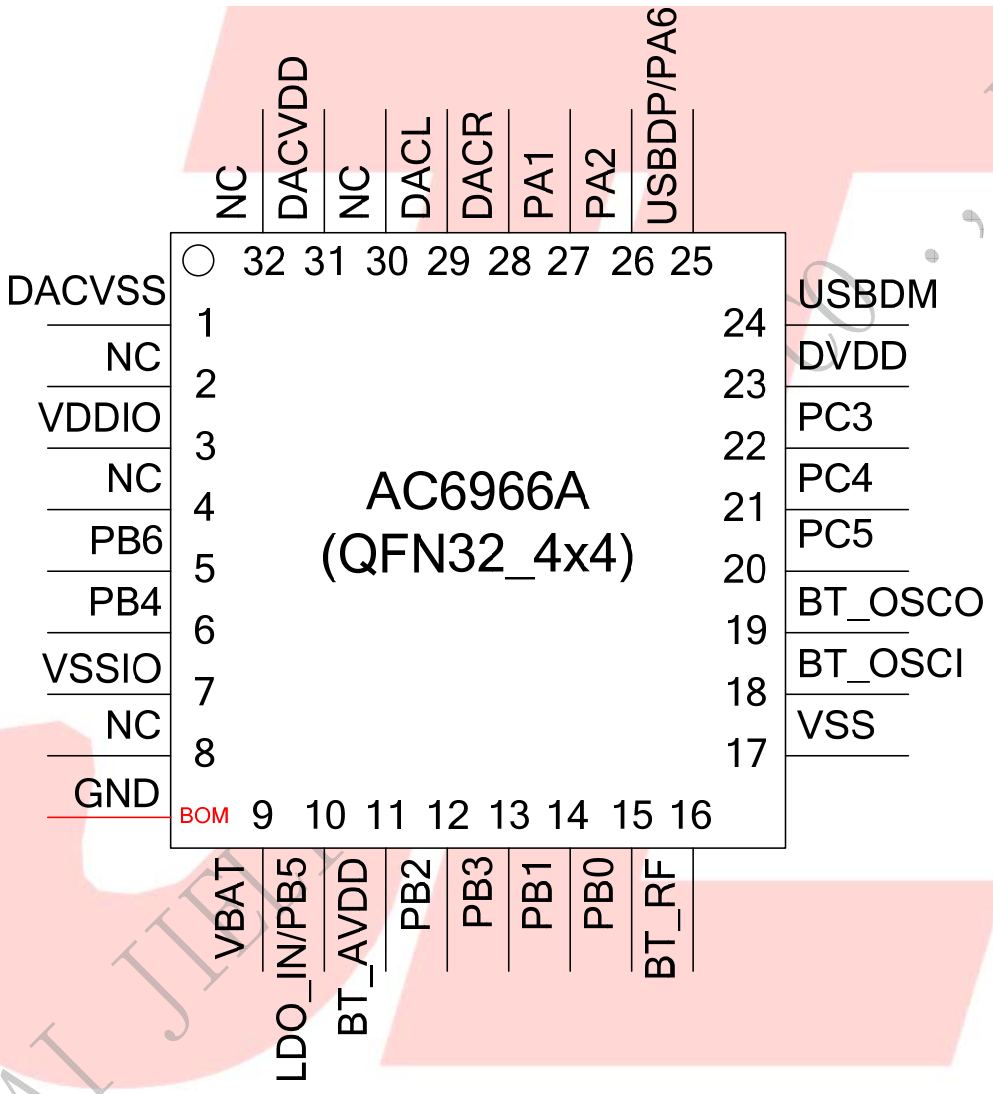


Figure 1-1 AC6966A_QFN32 Package Diagram

1.2 Pin Description

Table 1-1 AC6966A_QFN32 Pin Description

PIN NO.	Name	I/O Type	Drive (mA)	Function	Other Function
1	DACVSS	P	/		DAC Ground
2	NC				
3	VDDIO	P	/		IO Power 3.3v
4	NC				
5	PB6	I/O	24/8	GPIO	AMUX1L：Analog Channel1 Left; SPI2CLKA：SPI2 Data Out(A); IIC_SCL_C：IIC SCL(C); ADC8：ADC Input Channel 8; TMR3：Timer3 Clock Input; UART1TXA：Uart1 Data Out(A);
6	PB4	I/O	24/8	GPIO	SPI0_DAT2A(2)：SPI0 Data2 Out_A(2); ADC7：ADC Input Channel 7; CLKOUT1 UART2TXC：Uart2 Data Out(C); UART2RXC：Uart2 Data In(C);
7	VSSIO	P	/		Ground
8	NC				
9	VBAT	P	/		Battery Power Supply
10	LDO_IN	P	/		Battery Charger In
	PB5	I/O	8	GPIO (High Voltage Resistance)	SPI2DIA：SPI2 Data Input(A)； PWM3：Timer3 PWM Output； CAP1：Timer1 Capture; UART0TXC：Uart0 Data Out(C)； UART0RXC：Uart0 Data In(C);
11	BT_AVDD	P	/		BT Power
12	PB2	I/O	8	GPIO (High Voltage Resistance)	SPI1DIA：SPI1 Data In(A); CAP0：Timer0 Capture; UART2TXB：Uart2 Data Out (B)；
13	PB3	I/O	24/8	GPIO	ADC6：ADC Input Channel 6; PWM2：Timer2 PWM Output； UART2RXB：Uart2 Data In(B);
14	PB1	I/O	24/8	GPIO (pull up)	Long Press Reset； SPI1DOA：SPI1 Data Out(A); ADC5：ADC Input Channel 5;

					TMR2：Timer2 Clock Input; UART0RXB：Uart0 Data In(B);
15	PB0	I/O	8	GPIO (High Voltage Resistance)	SPI1CLKA：SPI1 Clock(A); UART0TXB：Uart1 Data Out(B); TMR5：Timer5 Clock Input;
16	BT_RF	/	/		BT Antenna
17	VSSIO	P	/		Ground
18	BT_SOCI	I	/		BT OSC In
19	BT_SOCO	O	/		BT OSC Out
20	PC5	I/O	24/8	GPIO	SPI1DOB：SPI1 Data Out(B); IIC_SDA_B：IIC SDA(B); ADC12：ADC Input Channel 12; TMR1：Timer1 Clock Input; UART2RXD：Uart2 Data In(D);
21	PC4	I/O	24/8	GPIO	SPI0_DAT3AB(3)：SPI0 Data3(AB); SPI1CLKB：SPI1 Clock(B); IIC_SCL_B：IIC SCL(B); ADC11：ADC Input Channel 11; PWM1：Timer1 PWM Output; UART2TXD：Uart2 Data Out (D);
22	PC3	I/O	24/8	GPIO	SPI0_DAT2B(2)：SPI0 Data2(B); SPI1DIB：SPI1 Data In(B); CAP2：Timer2 Capture; UART0TXD：Uart0 Data Out (D); UART0RXD：Uart0 Data In(D);
23	DVDD	P	/		Core Power
24	USBDM	I/O	4	USB Negative Data (pull down)	SPI2DOB：SPI2 Data Out(B); IIC_SDA_A：IIC SDA(A); ADC14：ADC Input Channel 14; UART1RXD：Uart1 Data In(D);
25	USBDP	I/O	4	USB Positive Data (pull down)	SPI2CLKB：SPI2 Clock(B); IIC_SCL_A：IIC SCL(A); ADC13：ADC Input Channel 13; UART1TXD：Uart1 Data Output(D);
	PA6		24/8		IIC_SDA_D：IIC SDA(D); ADC4：ADC Input Channel 4; CAP4：Timer4 Capture; UART0RXA：Uart0 Data In(A);
26	PA2	I/O	24/8	GPIO	MIC_BIAS：Microphone Bias Output CAP3：Timer3 Capture;
27	PA1	I/O	24/8	GPIO	MIC：MIC Input Channel ; ADC1：ADC Input Channel 1;

					PWM4：Timer4 PWM Output； UART1RXC：Uart0 Data In(C);
28	DACR	O	/		DAC Right Channel
29	DACL	O	/		DAC Left Channel
30	NC				
31	DACVDD	P	/		DAC Power
32	NC				

2、Electrical Characteristics

2.1 Absolute Maximum Ratings

Table 2-1

Symbol	Parameter	Min	Max	Unit
Tamb	Ambient Temperature	-20	+70	°C
Tstg	Storage temperature	-65	+150	°C
VBAT	Supply Voltage	2.2	5.5	V
$V_{3.3IO}$	3.3V IO Input Voltage	-0.3	VDDIO+0.3	V
LDO_IN	Charge Input Voltage	-0.3	5.5	V

2.2 PMU Characteristics

Table 2-2

Symbol	Parameter	Min	Typ	Max	Unit	Test Conditions
LDO_IN	Loading current	_	_	300	mA	VBAT = 4.2V
VBAT	Voltage Input	2.2	3.7	5.5	V	
V_{DVDD}	Voltage output	0.9	1.2	1.25	V	VBAT = 4.2V, 30mA loading
V_{VDDIO}	Voltage output	_	3.3	_	V	VBAT = 4.2V, 100mA loading
V_{BT_AVDD}	Voltage output		1.3		V	VBAT=4.2V, 100mA loading
V_{DACVDD}	DAC Voltage	_	3.1	_	V	VBAT = 4.2V, 10mA loading

2.3 IO Input/Output Electrical Logical Characteristics

Table 2-3

IO input characteristics						
Symbol	**Parameter**	**Min**	**Typ**	**Max**	**Unit**	**Test Conditions**
V_{IL}	Low-Level Input Voltage	-0.3	_	0.3* VDDIO	V	VDDIO = 3.3V
V_{IH}	High-Level Input Voltage	0.7* VDDIO	_	VDDIO+0.3	V	VDDIO = 3.3V
IO output characteristics						
V_{OL}	Low-Level Output Voltage	_	_	0.33	V	VDDIO = 3.3V
V_{OH}	High-Level Output Voltage	2.7	_	_	V	VDDIO = 3.3V

2.4 Internal Resistor Characteristics

Table 2-4

Port	General Output	High Drive	Internal Pull-Up Resistor	Internal Pull-Down Resistor	Comment
PA1~PA6 PB1 PB4~PB7 PC0~PC5	8mA	24mA	10K	10K	1、PB1 default pull up 2、USBDM & USBDP default pull down 3、internal pull-up/pull-down resistance | accuracy ±20%
PA0,PB3	24	_	10K	10K	
PB0,PB2 PB5	8mA	_	10K	10K	
USBDP	4mA	_	1.5K	15K	
USBDM	4mA	_	180K	15K	

2.5 DAC Characteristics

Table 2-5

Parameter	Min	Typ	Max	Unit	Test Conditions
Frequency Response	20	_	20K	Hz	1KHz/0dB 10Kohm loading With A-Weighted Filter
THD+N	_	-75	_	dB	
S/N	_	95	_	dB	
Crosstalk	_	-90	_	dB	
Output Swing		1		Vrms	
Dynamic Range		90		dB	1KHz/-60dB 10Kohm loading With A-Weighted Filter
DAC Output Power	11		_	mW	32ohm loading

2.6 ADC Characteristics

Table 2-6

Parameter	Min	Typ	Max	Unit	Test Conditions
Dynamic Range		80		dB	1KHz/-60dB
S/N	_	90	91	dB	1KHz/-60dB
THD+N	_	-70	_	dB	
Crosstalk	_	-90	_	dB	

2.7 BT Characteristics

2.7.1 Transmitter

Basic Data Rate **Table 2-7**

Parameter		Min	Typ	Max	Unit	Test Conditions
RF Transmit Power			4	6	dBm	25℃, Power Supply VBAT=5V 2441MHz
RF Power Control Range			20		dB	
20dB Bandwidth			950		KHz	
Adjacent Channel Transmit Power	+2MHz		-40		dBm	
	-2MHz		-38		dBm	
	+3MHz		-44		dBm	
	-3MHz		-35		dBm	

Enhanced Data Rate **Table 2-8**

Parameter		Min	Typ	Max	Unit	Test Conditions
Relative Power			-1		dB	25℃, Power Supply VBAT=5V 2441MHz
π/4 DQPSK Modulation Accuracy	DEVM RMS		6		%	
	DEVM 99%		10		%	
	DEVM Peak		15		%	
Adjacent Channel Transmit Power	+2MHz		-40		dBm	
	-2MHz		-38		dBm	
	+3MHz		-44		dBm	
	-3MHz		-35		dBm	

2.7.2 Receiver

Basic Data Rate **Table 2-9**

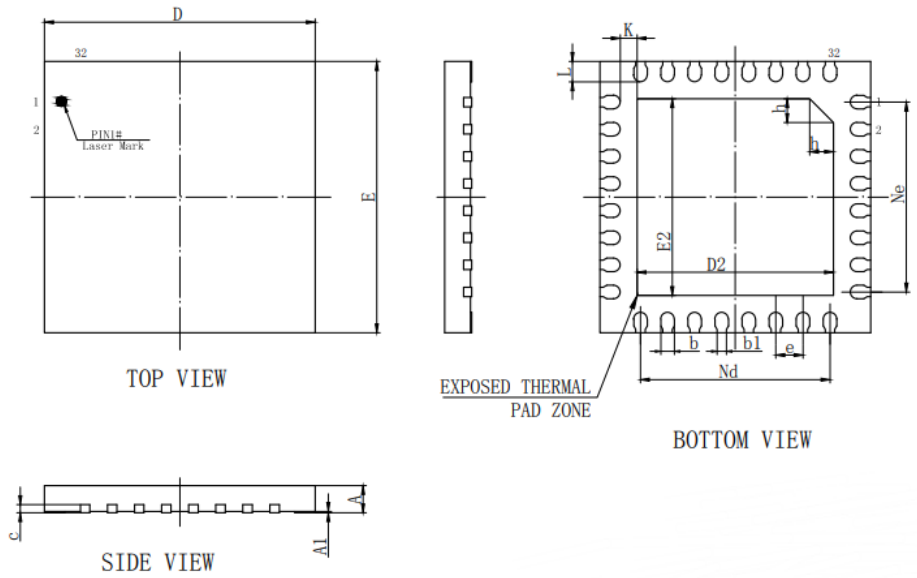
Parameter		Min	Typ	Max	Unit	Test Conditions
Sensitivity			-90		dBm	25℃, Power Supply VBAT=5V 2441MHz
Co-channel Interference Rejection			-13		dB	
Adjacent Channel Interference Rejection	+1MHz		+5		dB	
	-1MHz		+2		dB	
	+2MHz		+37		dB	
	-2MHz		+36		dB	
	+3MHz		+40		dB	
	-3MHz		+35		dB	

Enhanced Data Rate **Table 2-10**

Parameter		Min	Typ	Max	Unit	Test Conditions
Sensitivity			-90		dBm	25℃, Power Supply VBAT=5V 2441MHz
Co-channel Interference Rejection			-13		dB	
Adjacent Channel Interference Rejection	+1MHz		+5		dB	
	-1MHz		+2		dB	
	+2MHz		+37		dB	
	-2MHz		+36		dB	
	+3MHz		+40		dB	
	-3MHz		+35		dB	

3、 Package Information

3.1 QFN32(4mm*4mm)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.28	0.30	0.32
A1	0	0.02	0.05
b	0.15	0.20	0.25
b1	0.14REF		
c	0.102REF		
D	3.90	4.00	4.10
D2	2.80	2.90	3.00
e	0.40BSC		
Nd	2.80BSC		
E	3.90	4.00	4.10
E2	2.80	2.90	3.00
Ne	2.80BSC		
K	0.25REF		
L	0.25	0.30	0.35
h	0.30	0.35	0.40

Figure 3-1. AC6966A_QFN32 Package

3、Revision History

Date	Revision	Description
2020.03.14	V1.0	Initial Release